KVPX[®] APPLICATION GUIDE



1. PRODUCT INFORMATION

DAUGHTERCARD MODULES

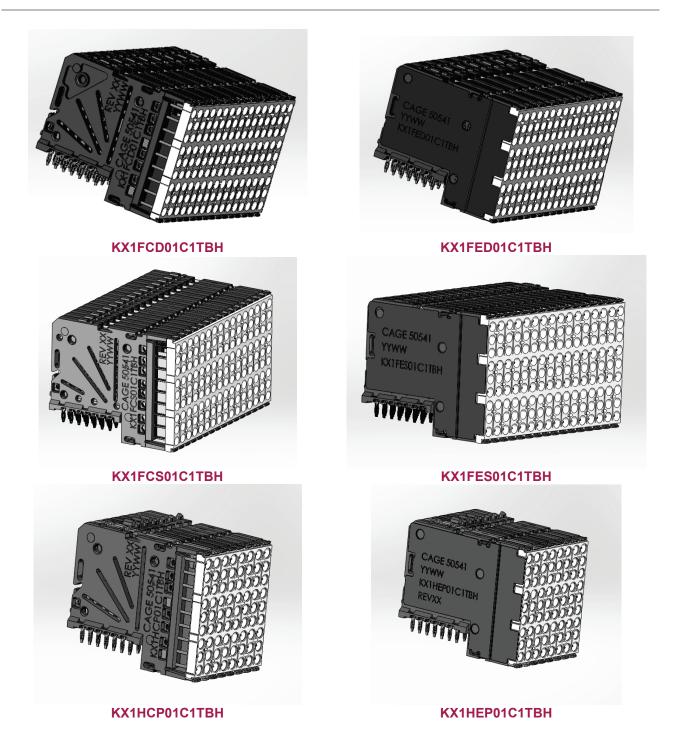
P.N.	Description	End Shield (Y/N)	Mating KX2 Module
KX1FCD01C1TBH	Daughtercard Center Differential Module	N	
KX1FED01C1TBHDaughtercard End Differential ModuleKX1FCS01C1TBHDaughtercard Center Single Ended Module		Y	KX2FCU01C1TAH
		N	KAZECOULCITAN
KX1FES01C1TBH	Daughtercard End Single Ended Module	Y	
KX1HCP01C1TBH	Daughtercard Center Utility Module	N	KX2HCU01C1TAH
KX1HEP01C1TBH	Daughtercard End Utility Module	Y	KAZIICOUTCTTAH

BACKPLANE MODULES

P.N.	Description	Equivalent Part Numbers
KX2FCU01C1TAH	144-Pin Female Connector	N/A
KX2HCU01C1TAH	72-Pin Female Connector	KX2HEP01C1TAH

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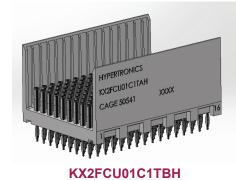
DAUGHTERCARD MODULES



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Application Specification for KVPX S50807

BACKPLANE MODULES





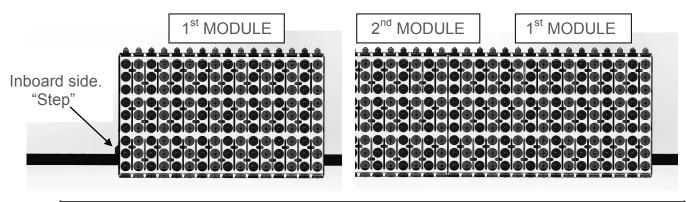
2. INSTALLATION TOOLING

INDIVIDUAL MODULE TOOLS

P.N.	Description	Compatible Modules
		KX1FCD01C1TBH
T2060 04	DAUGHTERCARD MODULE INSTALLATION	KX1FED01C1TBH
T3060-04	TOOL, 144-PIN**	KX1FCS01C1TBH
		KX1FES01C1TBH
T3060-03	DAUGHTERCARD MODULE INSTALLATION	KX1HCP01C1TBH
13060-03	TOOL, 72-PIN**	KX1HEP01C1TBH
T3055	BACKPLANE MODULE INSTALLATION TOOL, 144-SOCKET*	KX2FCU01C1TAH
T3079	BACKPLANE MODULE INSTALLATION TOOL, 72-SOCKET *	KX2HCU01C1TAH

**Only to be used with single module applications.

*For applications with adjacent module it is necessary to ensure that the first module is installed with the plastic "step" on the inboard side of the connector. All adjacent modules are to be installed in order following the same rule. See below:



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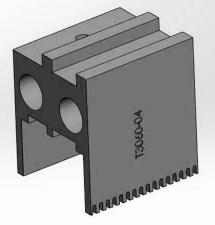
3U/6U TOOLS

P.N.	Description
T3099	3U INSTALLATION TOOL, BACKPLANE MODULES
T3099 & T3100	6U INSTALLATION TOOLS, BACKPLANE MODULES
T3102	3U INSTALLATION TOOL, DAUGHTERCARD MODULE
T3102 & T3103	6U INSTALLATION TOOLS, DAUGHTERCARD MODULE

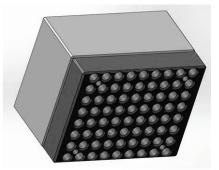
APPLICATION FIXTURE

P.N.	Description	
T3101	6U DAUGHTERCARD FIXTURE W/ BASE	

TOOLS



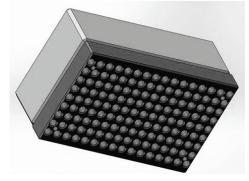
T3060-04



T3079



T3060-03



T3055

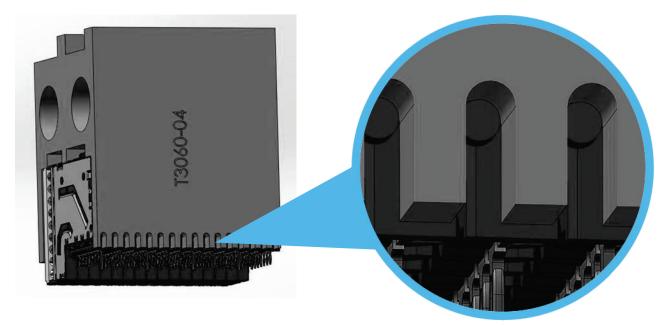
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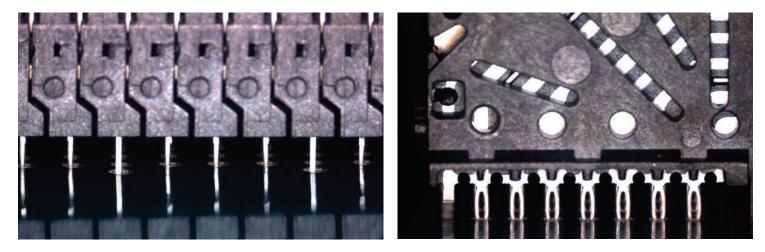


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- 3. **INSTALLATION** (Caution: please wear safety glasses)
 - 3.1 DAUGHTERCARD MODULE
 - **3.1.1** Fit connector module to tool. Ensure that location posts fit into tool comb as shown.



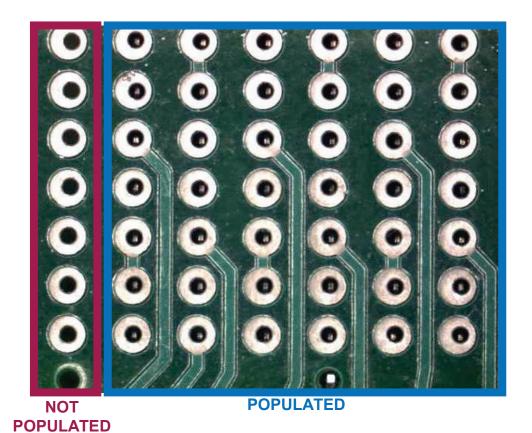
3.1.2 Align compliant terminals with printed circuit board.



Compliant terminals must be aligned with plated thru-holes

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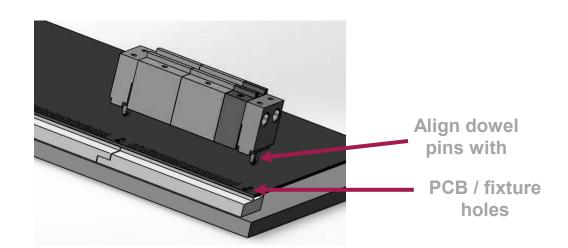
3.1.3 Mount PCB/Connector/Tool on application fixture. Application fixture is intended to provide proper alignment of tools and connectors during pressing operation. If pressing in a 3U/6U assembly, align dowel pins with holes on PCB and fixture.

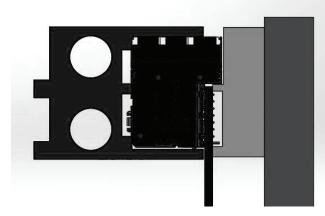
NOTE: Application fixture is designed for PCB of 1.80-1.90mm thickness. For additional PCB thicknesses contact Smiths Connectors Technical Services.

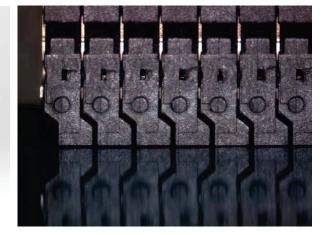
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3.1.4 Apply steady force to center of tool. Make certain that there is no gap between the PCB and connector. Approximate force per full 16 wafer module is:
250 lbsf (SnPb via)
350 lbsf (ENIG via)





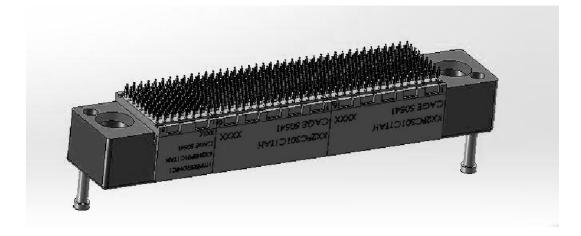


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3.2 Backplane Module

Fit connector module(s) to tool. Sockets will fit into the tool cavities.

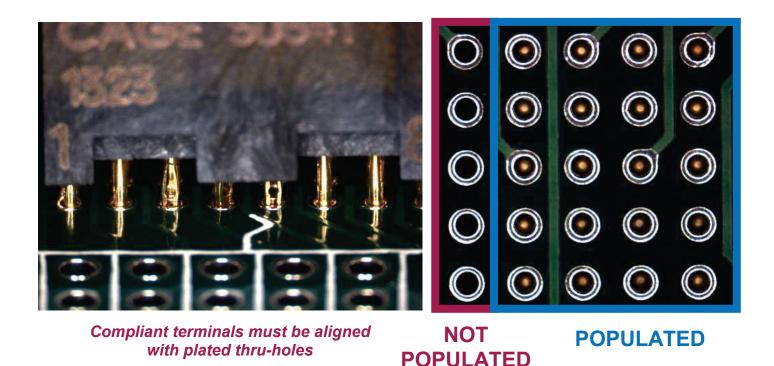


3.2.1 Align compliant terminals with PCB. Inspect PCB from reverse side and ensure that the compliant terminals are aligned with plated thru-holes. If pressing in a 3U/6U assembly, align assembly tool with guide pins on PCB.

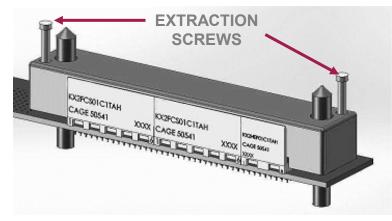


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Application Specification for KVPX S50807



3.2.2 Apply steady force to center of tool. Make certain that there is no gap between the PCB and connector. To remove the tool, evenly lift tool off of the connector or, if using the optional jackscrews, evenly turn both extraction screws clockwise to press tool off of connector.



Approximate installation force per full 144 contact module: 75 lbsf (SnPb via) 125 lbsf (ENIG via)

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4. PRINTED CIRCUIT BOARD

4.1 DAUGHTERCARD

- **4.1.1 PC Board:** Minimum board thickness of 1.60mm [0.063in]. Maximum allowable bow of PCB shall be 0.03mm [0.001in] over the length of each individual module.
- **4.1.2 Manufacturing tolerance for ø0.46±0.05mm finished hole with SnPb plating:** Drilled Hole = ø0.55 ± 0.02mm Cu Plating = 0.025 - 0.050mm SnPb Plating = 0.0038mm - 0.0124mm
- 4.1.3 Manufacturing tolerance for $\emptyset 0.46 \pm 0.05$ mm finished hole with ENIG plating: Drilled Hole = $\emptyset 0.55 \pm 0.02$ mm Cu Plating = 0.025 - 0.050mm ENIG Plating = 0.0001 - 0.0005mm (Au) over 0.00127 - 0.0076mm (Ni)
- 4.1.4 Manufacturing tolerance for ø0.46±0.05mm finished hole with ENIG plating per IPC-4552:

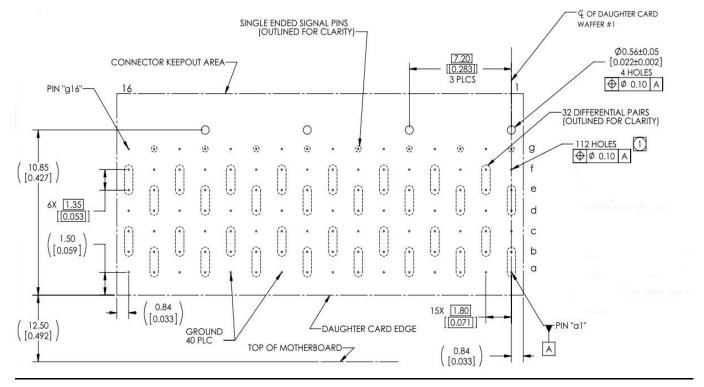
Drilled Hole = Ø0.55 ± 0.02mm Cu Plating = 0.025 - 0.050mm ENIG Plating = 0.00005 mm – no max (Au) over 0.0030 - 0.0060mm (Ni)

4.1.5 PCB mounting pattern:

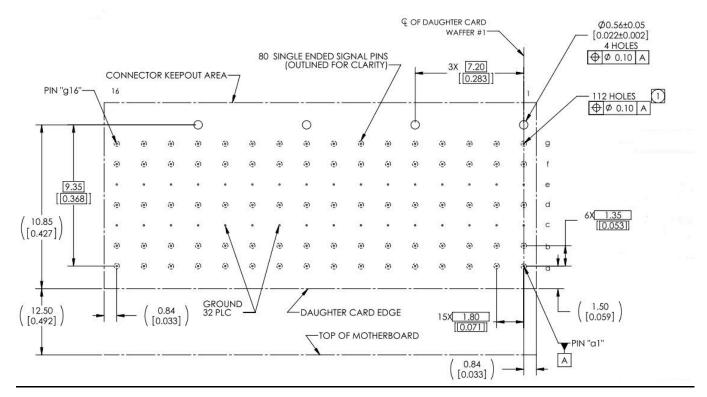
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DIFFERENTIAL MODULE

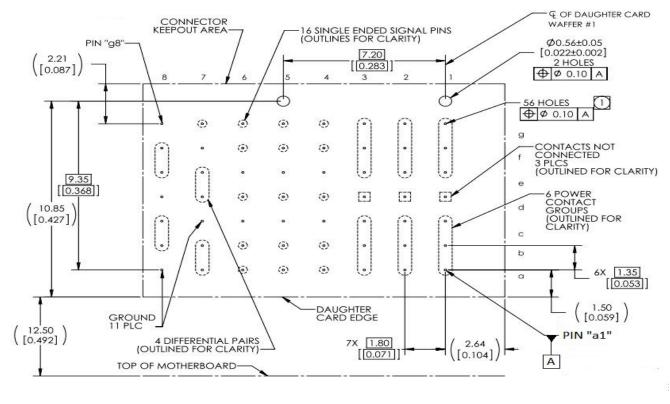


SINGLE-ENDED MODULE



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4.2 BACKPLANE

4.2.1 Manufacturing tolerance for $\emptyset 0.56 \pm 0.05$ mm finished hole with SnPb plating: Drilled Hole = $\emptyset 0.65 \pm 0.02$ mm

Cu Plating = 0.025 - 0.050mm SnPb Plating = 0.0038mm - 0.0124mm

4.2.2 Manufacturing tolerance for Ø0.56 ± 0.05mm finished hole with ENIG plating: Drilled Hole = Ø0.65 ± 0.02mm Cu Plating = 0.025 - 0.050mm ENIG Plating = 0.0001 - 0.0005mm (Au) over 0.00127 - 0.0076mm (Ni)

4.2.3 Manufacturing tolerance for Ø0.56 ± 0.05mm finished hole with ENIG plating per IPC-4552:

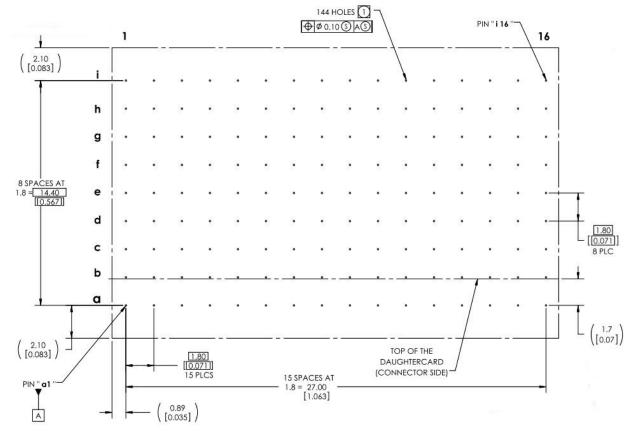
Drilled Hole = Ø0.65 ± 0.02mm Cu Plating = 0.025 - 0.050mm ENIG Plating = 0.00005mm – no max (Au) over 0.0030 - 0.0060mm (Ni)

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4.2.4 PCB mounting pattern:

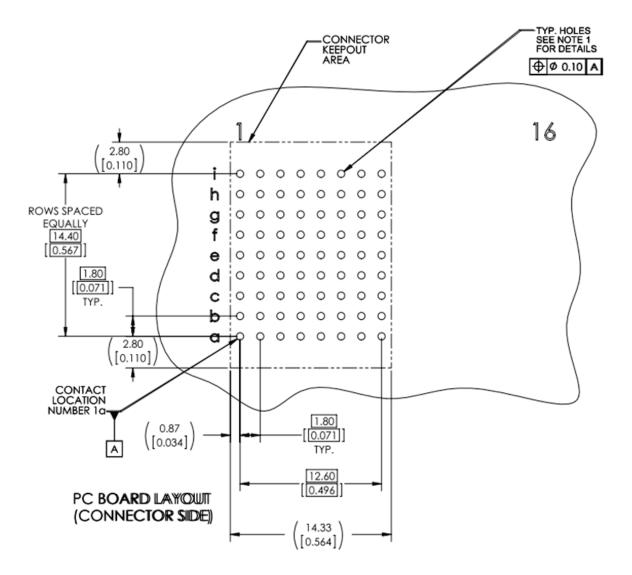
FULL MODULE



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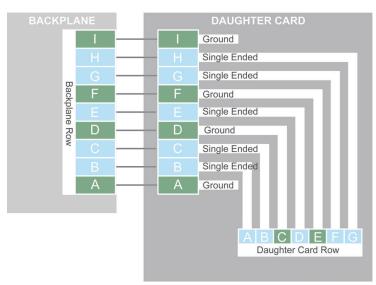
HALF MODULE



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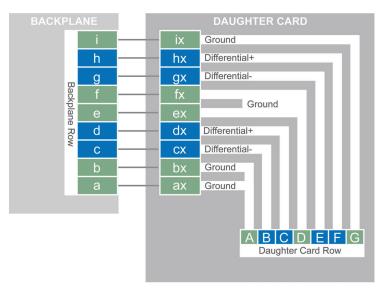
5. MODULE DETAILS

SINGLE ENDED



Single-ended wafer to backplane pin mapping

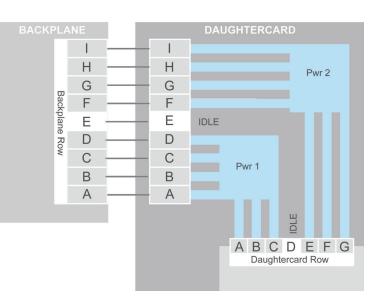
EVEN WAFER



Even differential wafer backplane pin mapping

Odd differential wafer backplane pin mapping

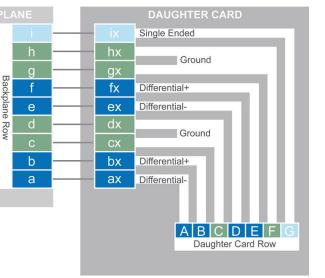
POWER



Power wafer backplane pin mapping

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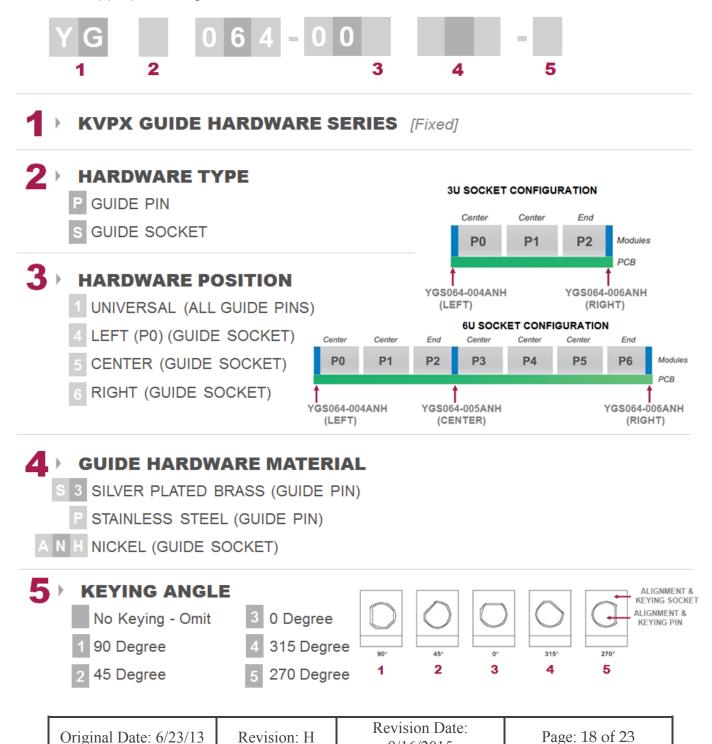
ODD WAFER



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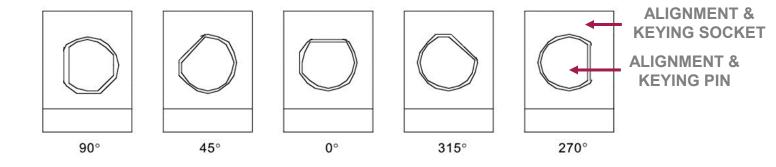
6. GUIDE HARDWARE

6.1 Guide sockets and pins are available in the following keying options, including without keying (no flats). It is important to make sure that pin angles are matched to sockets on the appropriate angles.



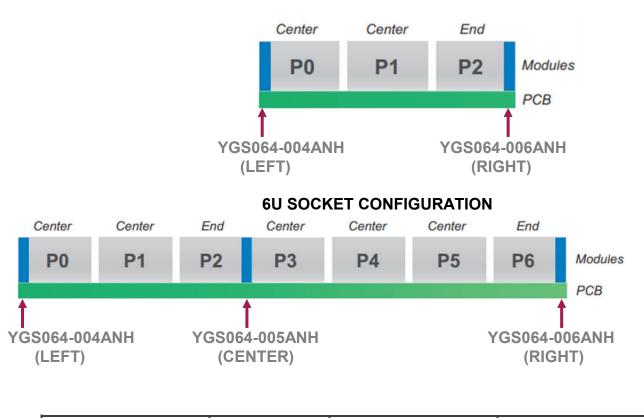
9/16/2015

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6.2 GUIDE SOCKET INSTALLATION

6.2.1 Press guide socket into PCB. Make certain there is no gap between PCB and guide socket. Install guide sockets in the arrangement indicated below (part number provided for reference). For custom configurations consult technical services.



3U SOCKET CONFIGURATION

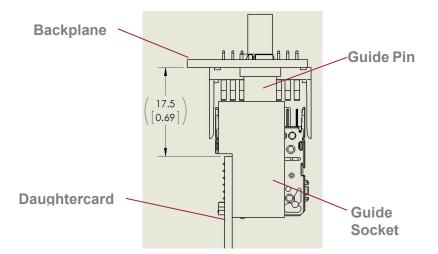
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- **6.2.2** Apply thread locker to screw threads
- 6.2.3 Tighten screw to a maximum of 25 in-oz
- 6.3 Guide pin installation
 - **6.3.1** Press guide pin into PCB. Make certain there is no gap between PCB and guide pin.
 - **6.3.2** Apply thread locker to threads of hex nut.
 - **6.3.3** Tighten hex nut to a maximum of 55 in-lbs.

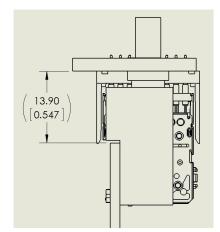
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7. MATING SEQUENCE

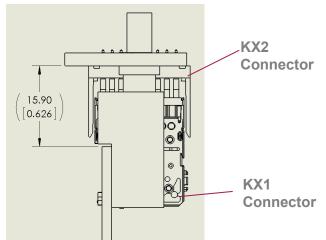
Mating Sequence 1 (Guide pin engages guide socket)



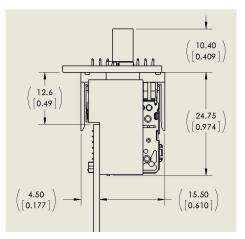
Mating Sequence 3 (Ground contacts engage)



Mating Sequence 2 (ESD contacts engage)



Mating Sequence 4 (Signal contacts fully engaged / fully mated)



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SMITHS CONNECTORS GLOBAL SUPPORT

AMERICAS

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info@smithsconnectors.com www.smithsconnectors.com

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Revision No:	Revision Date:	Revised By:	Description:
А	6/23/2013	Ian Dower	Initial Release
В	10/31/13	Ian Dower	Changes made to pages 9, 12, 14, 15, 18 and 19. New pinouts added, PCB thickness for application fixture, Daughter Card PCB patterns updated (All proposed changes are temporarily outlined with red dotted lines)
С	6/12/2014	Zach Pokornowski	Overall format changes. Updated P/N's, tooling lists, KX2 keep out zones, and installation forces. Additional PCB plating types added. Additional clarification of the mating sequence.
D	7/29/2014	Zach Pokornowski	Corrected Backplane tooling labels. Additional guide socket and guide pin part numbers
Е	3/30/2015	Zach Pokornowski	Added Guide Hardware How to Order Information and IPC-4552 Enig Plating Information. Clarrified torque for guide pin assembly.
F	6/8/2015	Ian Dower	Corrected installation force of KX1 module. Included ENIG forces. Added notes defining order of installation for KX1 modules.
G	7/17/2015	Ian Dower	Changed views to match description on pages 1 and 2 per attached markup file.
Н	9/16/2015	Scott Horner	Via holes not supported – removed sections 4.1.3 & 4.2.2

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